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TITLE: DATA ERROR DETECTING SYSTEM

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INVENTOR-INFORMATION:

NAME

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NAME

COUNTRY

FUJITSU LTD

N/A

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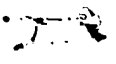
INT-CL (IPC): H04L001/00, H04L013/00

US-CL-CURRENT: 714/746, 714/800, 714/801

ABSTRACT:

PURPOSE: To detect surely a data error by transmitting the result of each addition of all data of an even number word and an odd number word of a transfer data together with the transfer data, and allowing the reception side to compare the coincidence/dissidence between each addition data of the transfer data and the added data of the transmitted even and odd number word.

CONSTITUTION: When a transmission request of data exists, a CPU1 executes a transmission request program and performs processing. That is, an addition processing added data additional program 18 adds all data of the even number word and the odd number word of the transfer data and stores the result to an even number check word area 7 and an odd check word area 8. Each stored addition data is added to the transfer data and transmitted to the reception side. A CPU9 executes a data reception program at the reception side and execute processing. That is, an addition processing added data comparison check program 20 adds all data of the even number word and the odd number word of the transfer data and stores the result to an even number check word data 14 and an odd number check word area 15. Each stored added data of the even and



odd number word are compared with the transmitted added data of the even and odd number words.

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